

## Professor LI Zhiqiang

College	College of Information Engineering (College of Artificial Intelligence)
Current Position	Professor
Types of Tutor	Master Tutor
Language	Chinese/English
Education	I received my BSc degree from Yangzhou University in 1996, Meng and Deng degrees from Southeast University in 2004 and 2010 respectively.
Research Interests	Quantum Information
Selected Publications	<ol style="list-style-type: none"><li>1.*Li, Zhiqiang, Sai Chen, Xiaoyu Song, Marek Perkowski, Hanwu Chen and Wei Zhu, Quantum Circuit Synthesis using a New Quantum Logic Gate Library of NCV Quantum Gates, International Journal of Theoretical Physics, 55(12), pp 1-18, Dec. 2016. (SCI)</li><li>2.Wei Zhu; *Li, Zhiqiang; Gaoman Zhang; Suhan Pan; Wei Zhang, A Reversible Logical Circuit Synthesis Algorithm Based on Decomposition of Cycle Representations of Permutations, International Journal of Theoretical Physics, 2018. (SCI)</li><li>3.*Li, Zhiqiang; Chen, Hanwu; Song, Xiaoyu, Perkowski, Marek, A Synthesis Algorithm for 4-Bit Reversible Logic Circuits with Minimum Quantum Cost, ACM Journal on Emerging Technologies in Computing Systems, 11(3), pp 29:1-29:18, Dec. 2014. (SCI)</li><li>4.*Li, Zhiqiang; Song, Xiaoyu; Perkowski, Marek; Chen, Hanwu; Feng, Xiaoxia, Realization of a new permutative gate library using controlled-kth-root-of-NOT quantum gates for exact minimization of quantum circuits, International Journal of Quantum Information, 12(5), pp 1450034-1-1450034-18, Aug. 2014. (SCI)</li><li>5.*Li, Zhiqiang, Hanwu Chen, Guowu Yang, and Wenjie Liu, Efficient Algorithms for Optimal 4-Bit</li></ol>

	<p>Reversible Logic System Synthesis, Journal of Applied Mathematics, Volume 2013 (2013), Article ID 291410, 8 pages, doi.org/10.1155/2013/291410(SCI)</p> <p>6.LI Zhi-Qiang, CHEN Han-Wu,XU Bao-Wen,LI Wen-Qian,WANG Jia-Jia,LIU Wen-Jie, A Fast Algorithm for Synthesis of Quantum Reversible Logic Circuits, Chinese Journal of Computers, 32 (7):1291-1303, 2009, (EI)</p> <p>7.LI Zhi-Qiang, LI Wen-Qian, CHEN Han-Wu, Algorithm of Optimizing Quantum Reversible Logic Synthesis, Chinese Journal of Software 20(9): 2332-2343, 2009, (EI)</p> <p>Zhiqiang Li, Hanwu Chen, Baowen Xu, Wenjie Liu,Xiaoyu Song, Xilin Xue, Fast algorithm for 4-qubit reversible logic circuits synthesis, IEEE World Congress on Computational Intelligence (WCCI2008), (Hong Kong, June 1-6,pp. 2202-2207, (EI)</p>
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